

Docket No.: 42390P5258D

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re the Application of:

SANJAY DABRAL, ET AL.

Application No.: 09/651,385

Filed: August 29, 2000

Diode and Transistor Design for High Speed I/O For:

Art Group: 2815

Examiner: Jose R. Diaz

## PETITION FOR EXTENSION OF TIME PURSUANT TO 37 C.F.R. § 1.136(a)

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

In accordance with 37 C.F. R. § 1.136(a), Applicants for the above-identified application respectfully Petition the Commissioner for a one (1) month extension of time, extending the period for response to November 12, 2004, from the Final Office Action dated July 12, 2004. The petition filing fee of \$110.00 and an Amendment and Response to Office Action are attached.

If it should be determined that a longer extension of time is required to prevent this application from being abandoned, please charge any additional fees to Deposit Account No. 02-2666. A copy of the Fee Transmittal is enclosed for deposit account charging purposes.

11/15/2004 HALI11 00000079 09651385

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Respectfully submitted,

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## **CERTIFICATE OF MAILING/TRANSMISSION**

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I hereby certify that this correspondence is being deposited with the United States Postal Service on the date shown below with sufficient postage as first class mail in an envelope addressed to: Mail Stop AF, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.